

**Amendment to the Specifications:**

**[0014.1]** In an aspect of the present invention, an electrical circuit is provided. The electrical circuit may comprise an amplifier circuit and an active bias circuit which are both formed on a wafer. The amplifier circuit may have a designed input biasing voltage for operating the amplifier circuit in an optimal range (e.g., linear range, etc.). The amplifier circuit may also have a required input biasing voltage offset to operate the amplifier circuit in the optimal range due to wafer lot variations.

**[0014.2]** The active bias circuit may be in electrical communication with the amplifier circuit. The active bias circuit may have a designed output biasing voltage and an actual output biasing voltage offset due to the wafer lot variations. The actual output biasing voltage offset may be proportional to the required input biasing voltage offset such that amplifier circuit operates in the optimal range, notwithstanding wafer lot variations.